SAMUEL GONSHAW							
0	sam@gonshaw.com 🖨 gonshaw.com 🖆 samgonshaw 🗘 sjg10						
RECENT EMPLOYMENT	<ul> <li>BIOS Health Ltd.</li> <li>Medical device startup developing AI-powered embedded neural interfaces.</li> <li>Embedded Systems Engineer, Cambridge, UK 2018–</li> <li>Architecture of embedded data collection systems driving 25+ live medical trials.</li> <li>Scoping, onboarding and managing edge systems for multisite deployment.</li> <li>Development of C++/Qt desktop applications providing clinical interfaces.</li> <li>Compiled ISO 14971 risk register to provide accountability and product safety.</li> <li>Development of IEC 62304 Class C software unlocking clinical device use.</li> <li>Full lifecycle development of FPGA solutions for highly accurate data collection.</li> <li>Integration with cloud hosted Python ML pipelines unlocking novel data insights.</li> <li>Managing interns in embedded testing projects, expanding the team.</li> <li>Authoring software lifecycle processes and implementing supporting systems used to drive productivity and project management across teams in a new company.</li> </ul>						
	Mathematics Tutor       2022–202         • Tutoring of GCSE and A-Level Mathematics on a 1–1 basis.       2022–202	23					
	Toshiba America Storage Research Design Centre / OCZ Storage SolutionMultinational developing SSDs for Consumer, Datacentre and Enterprise applicationSenior Firmware Engineer, Oxford, UK / San Jose, CA2015–201Firmware Engineer, Oxford, UK2014–201• Design of bare metal C firmware for enterprise SSDs released to market.• Design of ASIC verification firmware eliminating respin costs.• Development of hardware abstraction libraries saving development costs.• New feature development for old products revitalising sales.• Linux driver and test tooling development improving device reliability.• Lifetime product support for released products improving customer experience• Designing and performing characterisation experiments on flash technologies.• Liaising between geographically diverse teams on multidisciplinary development	<i>cs.</i> 18 15					
	Imperial College London <u>Research Assistant</u> 2012–201         • Individual research converting Masters thesis into publishable content.         • Python/MAGMA package creation for the Open Source SAGEMath project.         • Multi-threaded C application for weighted projective space characterisation.	15					
EDUCATION	Imperial College London2010–201• MSci. incorporating BSc. ARCS 1 <sup>st</sup> Class (Hons.) Mathematics	14					
	Birkat Moshe Talmudic Seminary200• Jewish theology with emphasis on Talmudic texts.	)7					
PUBLICATIONS	<ul> <li>Using neural biomarkers to personalize dosing of vagus nerve stimulation.</li> <li>Antonin Berthon et al. <i>Bioelectronic Medicine</i>, 10(1):15, Jun 2024</li> <li>Online bayesian optimization of vagus nerve stimulation.</li> <li>Lorenz Wernisch et al. <i>J. Neural Eng.</i>, 21(2):026019, April 2024</li> <li>Unipotent class representatives for finite classical groups.</li> <li>S. Gonshaw, M. Liebeck, and E.A. O'Brien. <i>J. of Group Theory</i>, 20(3):505–525, 201</li> <li>Further publications: https://orcid.org/0009-0009-4376-2711</li> </ul>						

PROCRAMMING         Embedded         Bare Metal/Multi-threaded Development         Fluent           AND TOOLING         C         Bare Metal/Multi-threaded Development         Intermediate           ARM, Xtensa ASM         Acceleration of Cittical Paths         Intermediate           HDI.         Python Generated FPGA Development         Intermediate           Applications         C++         C++17/STL App and Driver Development         Fluent           Applications         C++         C++17/STL App and Driver Development         Fluent           Configuration         YAML, XML, JSON         Intermediate         Putent           Build and Test         Crist         Unit Test Development         Fluent           Analysis/Debug         GDB, Valgrind, Prof. claug-format         Intermediate           Dodes         Build environment management         Intermediate           Modelsin,Verilator         Wave debugging for FPGA/ASIC         Intermediate           Dodes         Build Speed Buses         PCIe/NVMe, SATA, AMBA, USB, FT245         Intermediate           Low Speed Buses         PCIe/NVMe, SATA, AMBA, USB, FT245         Intermediate           Project Management         Glitab, Atlassin         Issue Tracking and Planning         Fluent           Gittab, Atlassin         Issue Tracking and Planning								
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